

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 114596-06-4003		SERIAL NO. 09/322,443	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT David L. Reese, et al.			
				FILING DATE May 28, 1999		GROUP ART UNIT 2122	

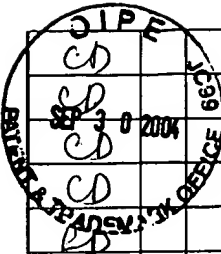
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
C.D	5,371,894	12/6/1994	DiBrino	714	34	Dec. 23, 1993
C.D	5,721,855	02/24/1998	Hinton	712	218	Jul. 12, 1996
C.D	5,729,728	03/17/1998	Colwell	712	234	Sep. 6, 1996
C.D	6,157,993	12/5/2000	Lewchuk	711	213	Jan. 3, 2000
C.D	6,256,775	7/3/2001	Flynn	717	127	Dec. 11, 1997
C.D	6,351,646	02/26/2002	Jellema	455	461	Apr. 20, 1998
C.D	6,535,903	3/18/2003	Yates	718	100	Jan. 29, 1996
C.D	6,553,431	04/22/2003	Yamamoto	710	8	Jul. 21, 1999
C.D	6,708,173	03/16/2004	Behr	707	10	Oct. 18, 2000
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES

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C.D		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264 (1999) //ftp.digital.com/pub/Digital/info/semiconductor/literature/cmpwrgd.pdf
C.D		Intel Corporation, Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture, pages 3-10 to 3-13, 7-1 to 7-3, 7-20 to 7-25; Volume 2: Instruction Set Reference, pages 3-60 to 3-63, 3-240 to 3-251; Volume 3: System Programming Guide, pages 11-1 to 11-3, 11-10 to 11-13, 14-1 to 14-3, 14-10 to 14-15 (1997)
C.D		Intel Corporation, Pentium Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20
C.D		Intel Corporation, Pentium Processor Family Developer's Manual (1997), vol. 3: Architecture and Programming Manual, pages 3-1 to 3-3, 3-10 to 3-13, 12-1 to 12-27, 14-1 to 14-30
C.D		Peterson, "The Profile Naming Service," ACM Transactions on Computer Systems (TOCS), v.6 n.4, p.341-364 (Nov. 1988)

EXAMINER <i>Chameli C. Don</i>	DATE CONSIDERED <i>3/2/05</i>
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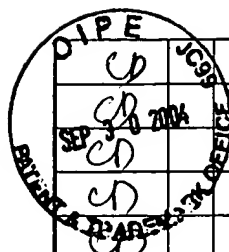
FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 114596-06-4003		SERIAL NO. 09/322,443	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT David L. Reese, et al.			
				FILING DATE May 28, 1999		GROUP ART UNIT 2122	
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CD	5,355,487	11/11/1994	Keller	717	127	Jul. 23, 1993	
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CD	5,483,647	01/09/1996	Yu	713	100	Dec. 17, 1992	
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CD	5,613,114	3/1997	Anderson	718	108	
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CD	5,764,947	06/09/1998	Murphy	703	20	Mar. 2, 1994
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CD	5,983,335	11/09/1999	Dwyer	712	23	Apr. 30, 1997
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CD	6,002,872	12/14/1999	Alexander	717	127	Mar. 31, 1998
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CD	6,119,218	9/2000	Arora	712	207	
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CD	6,154,857	11/28/2000	Mann	714	30	Dec. 17, 1997
CD	6,157,970	12/05/2000	Gafken	710	27	Sep. 24, 1997
CD	6,163,764	12/19/2000	Dulong	717	138	Oct. 12, 1998
CD	6,175,957	01/16/2001	Ju	717	156	Dec. 9, 1997
CD	6,202,205	03/13/2001	Saboff	717	9	Jul. 21, 1998
CD	6,205,545	03/20/2001	Shah	712	237	Apr. 30, 1998
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CD	6,233,678	05/15/2001	Bala	712	240	Nov. 5, 1998
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CD	6,240,526	05/29/2001	Petivan	714	11	Oct. 27, 1999
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CD	6,463,582	10/08/2002	Lethin	717	158	Oct. 21, 1998
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CD	6,473,846	10/29/2002	Melchior	711	170	Jun. 16, 2000
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C-D	WO 98/28689 Wing	07/02/1998	PCT/US	G06F	11/00		
C-D	WO 98/38575 Klaiber	09/03/1998	PCT/US	G06F	11/00		
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<div style="border: 1px solid black; border-radius: 50%; padding: 5px; display: inline-block;"> TYPE C-D SEP 30 2004 JCS9 OFFICE </div>		Reiner W. Hartenstein, Jürgen Becker: Performance Analysis in CoDe-X Partitioning for Structural Programmable Accelerators; Proc. of 5th Int'l Workshop on Hardware/Software Co-Design CODES/CASHE '97, Braunschweig, Germany, p. 141 (March 1997)
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